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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: : Date: 2/25/2004  
HADIZAD, PEYMAN : Group: 2811  
APPLN. NO. 10/623397 : Examiner:  
FILED: 7/18/2003 :

FOR: VERTICAL COMPOUND SEMICONDUCTOR FIELD EFFECT  
TRANSISTOR STRUCTURE

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING DEPOSITED WITH  
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Date of Deposit

SEMICONDUCTOR COMPONENTS INDUSTRIES, LLC

Name of Assignee

Lydia McNamara  
SIGNATURE

2/25/04  
DATE

**SUBMITTAL OF FORMAL DRAWINGS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Attn: Official Draftsman

SIR:

We are hereby submitting four (4) sheets of formal drawings  
for the above-identified patent application.

Respectfully submitted,

HADIZAD, PEYMAN

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